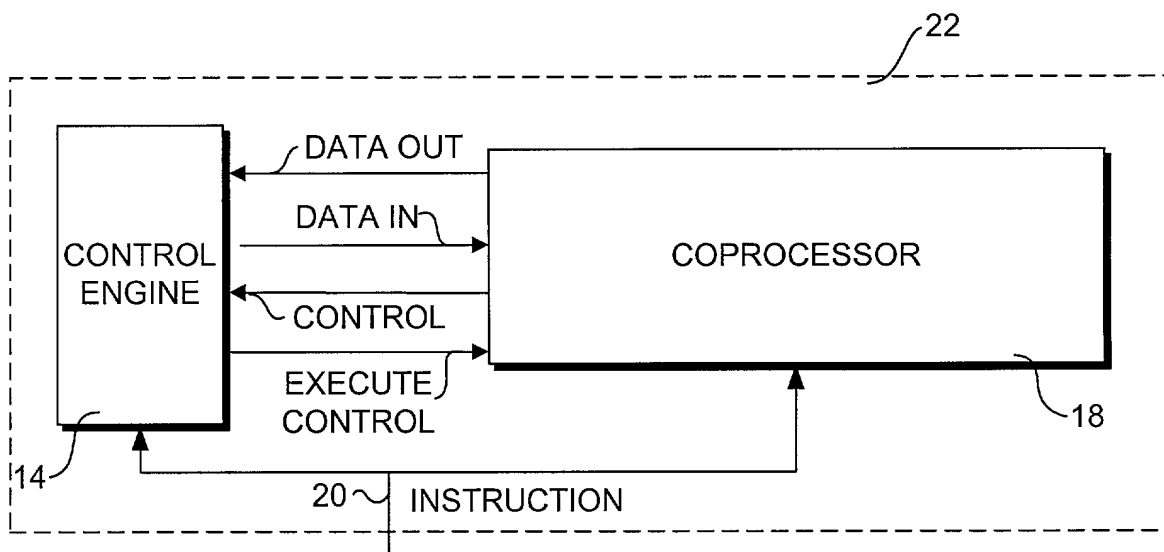
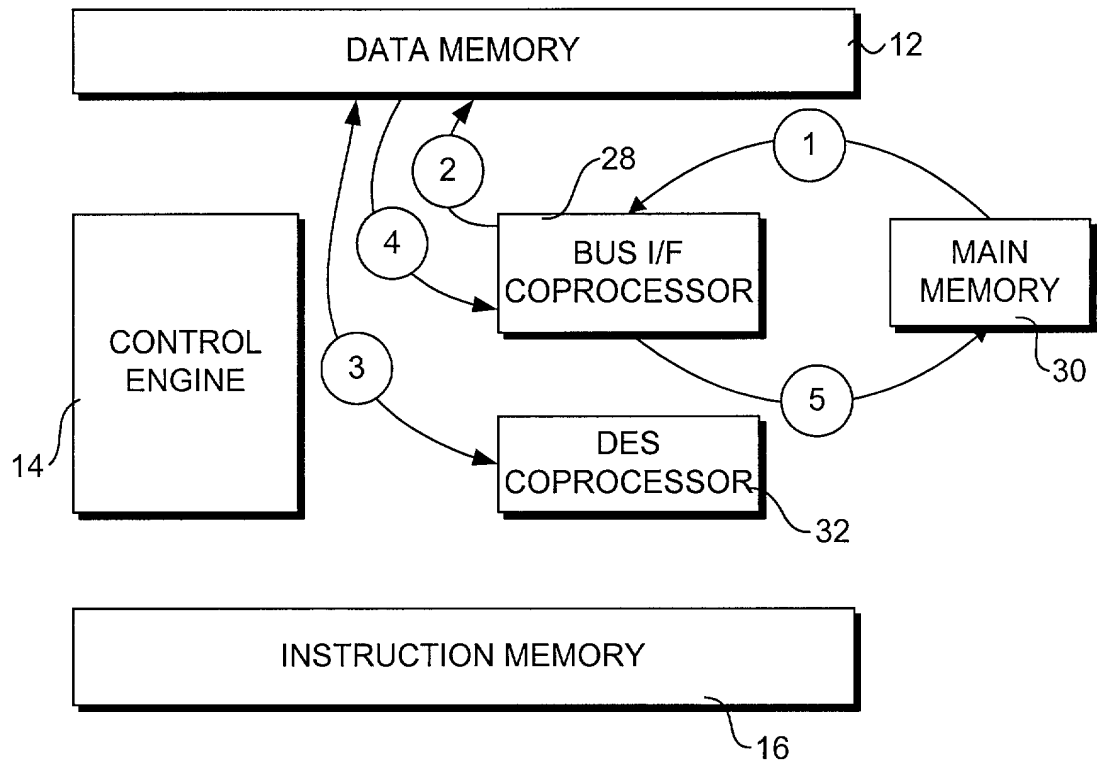
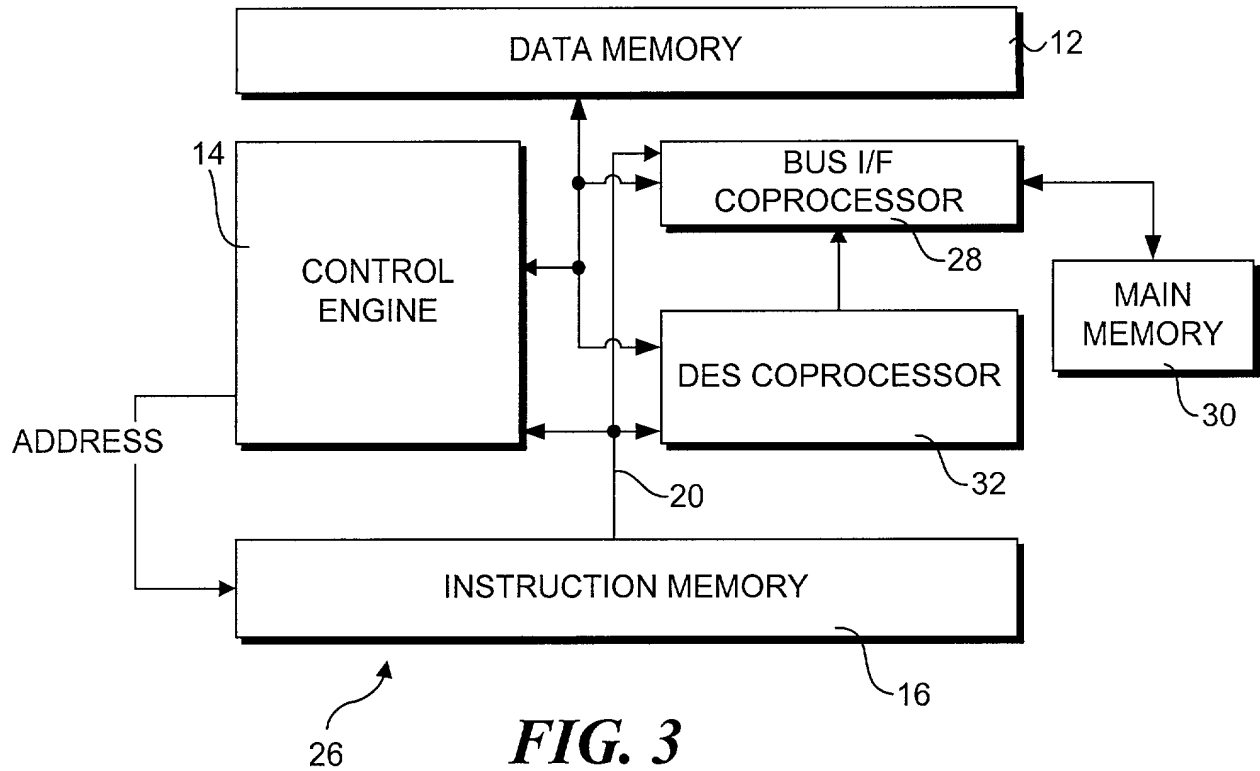
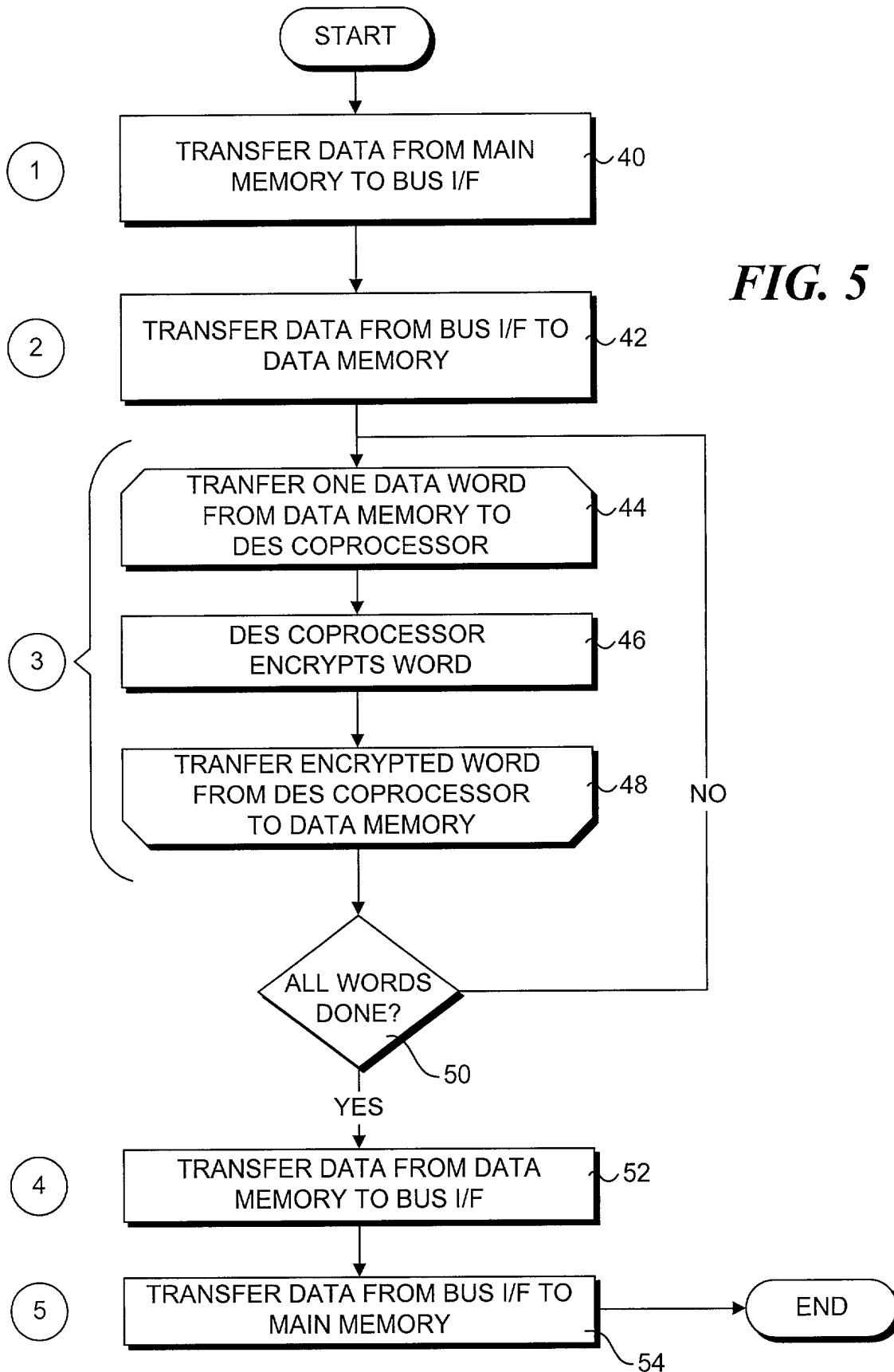


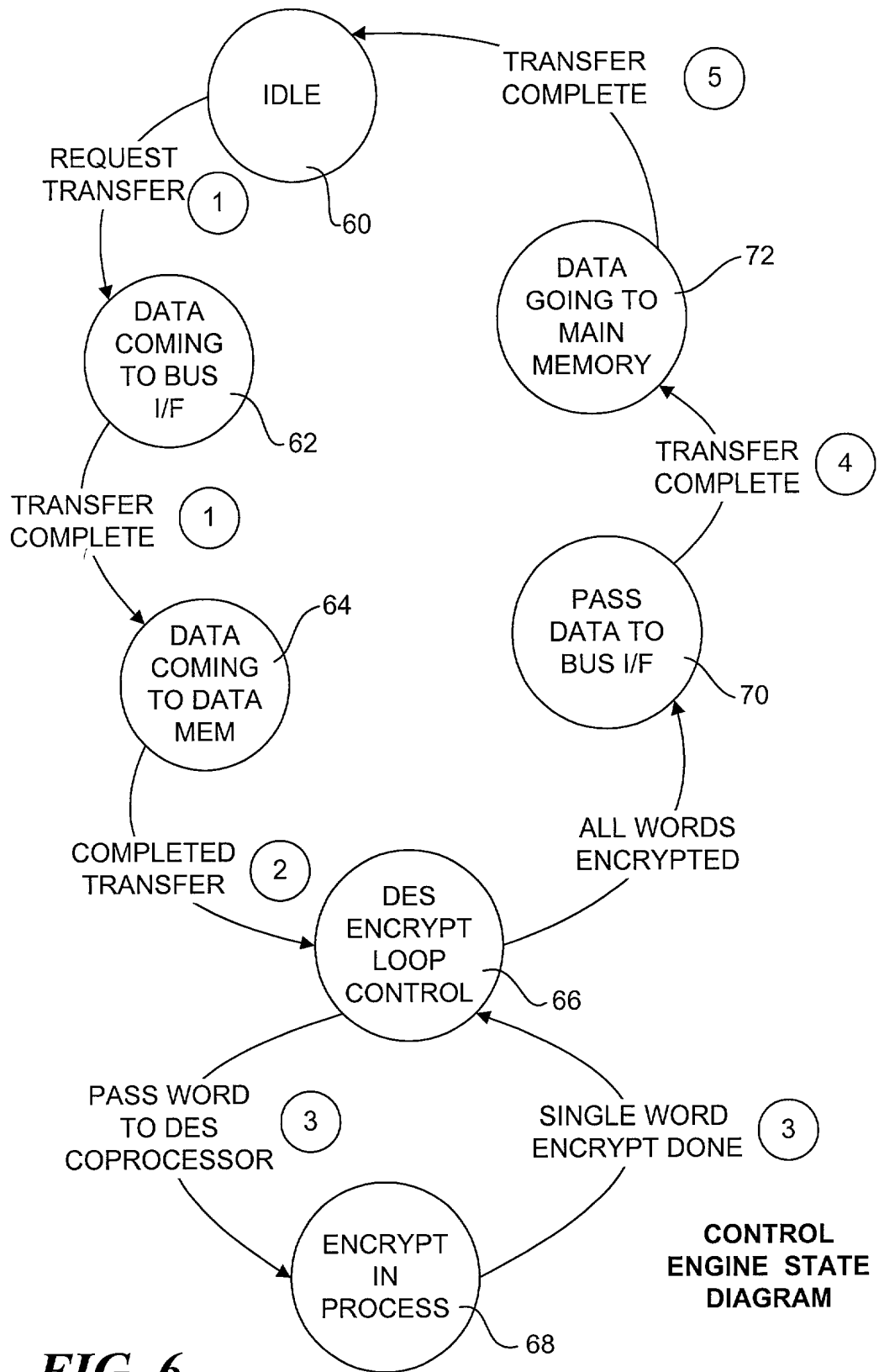
**FIG. 1**



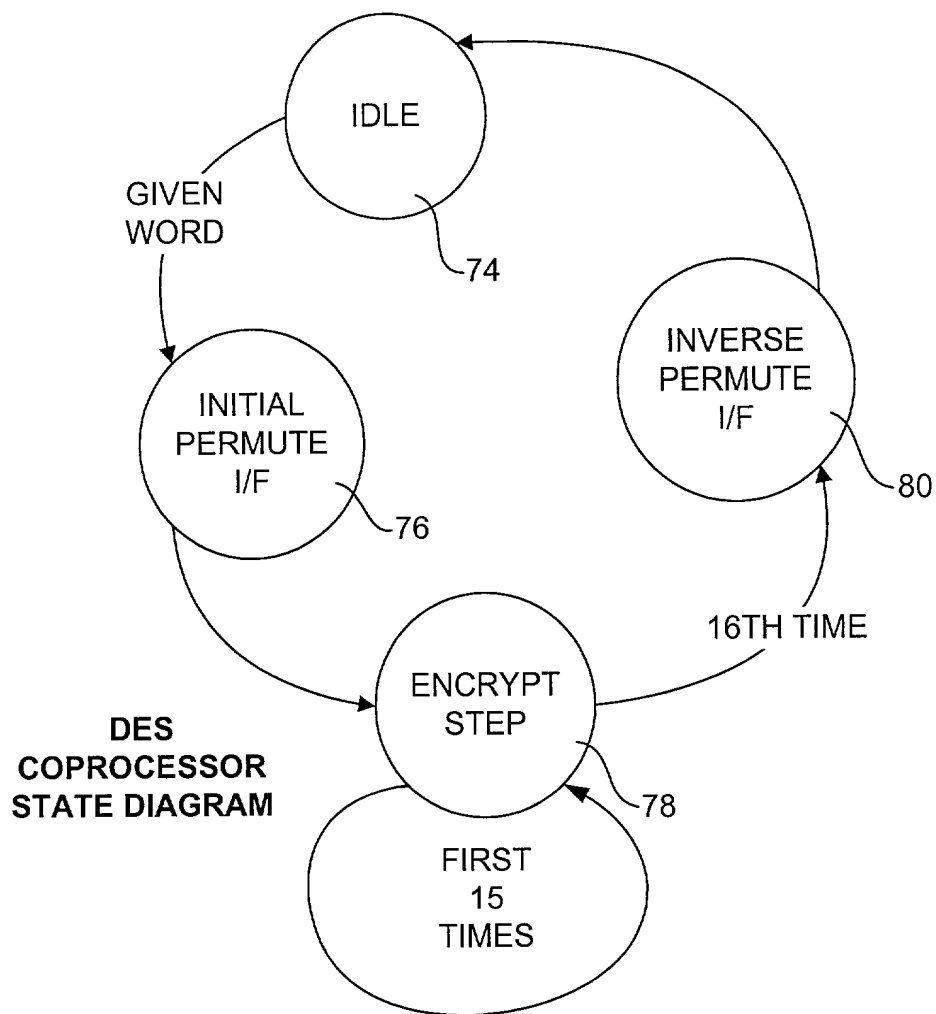
**FIG. 2**





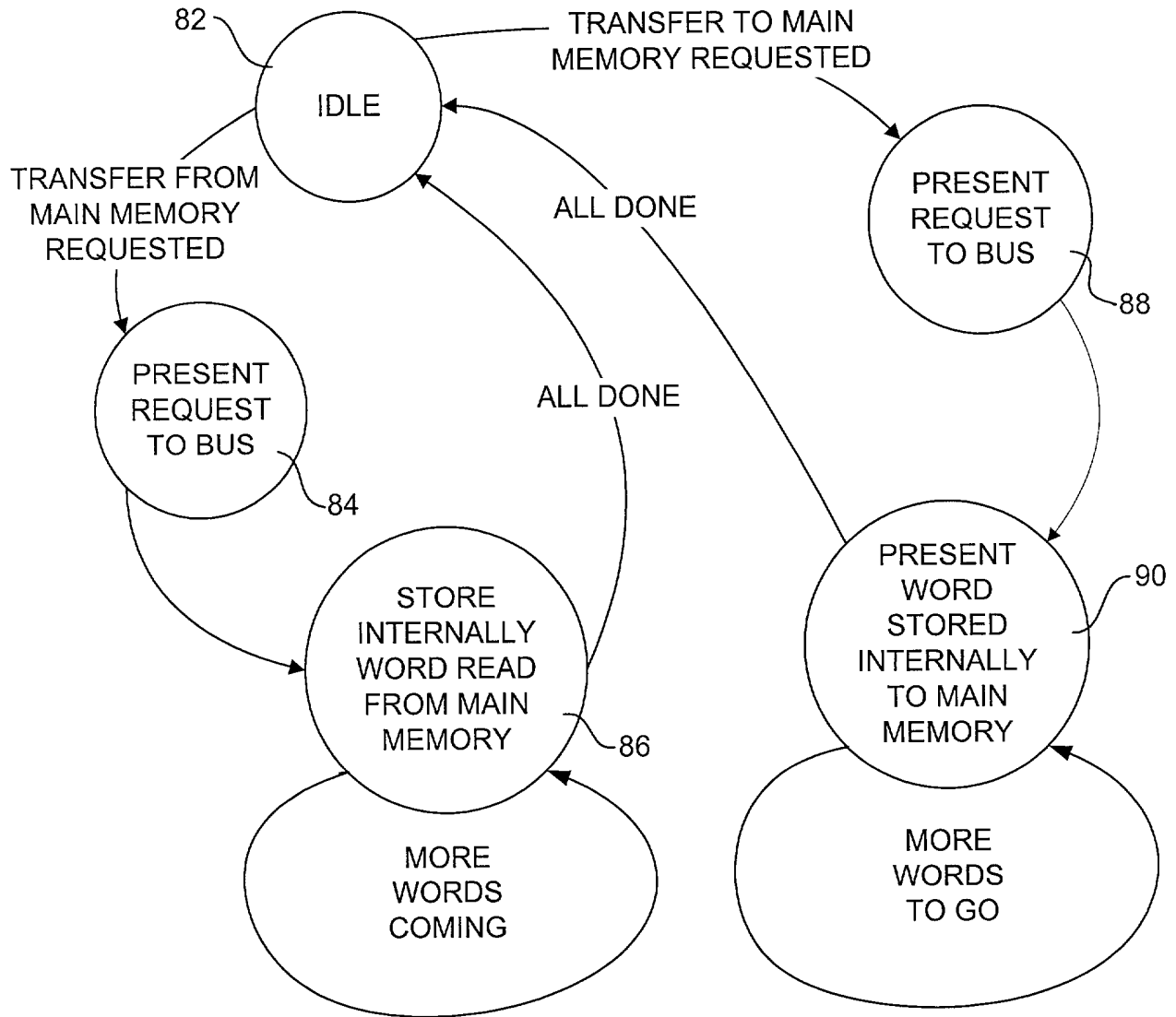


**FIG. 6**



**FIG. 7**

**BUS I/F  
COPROCESSOR  
STATE DIAGRAM**



**FIG. 8**

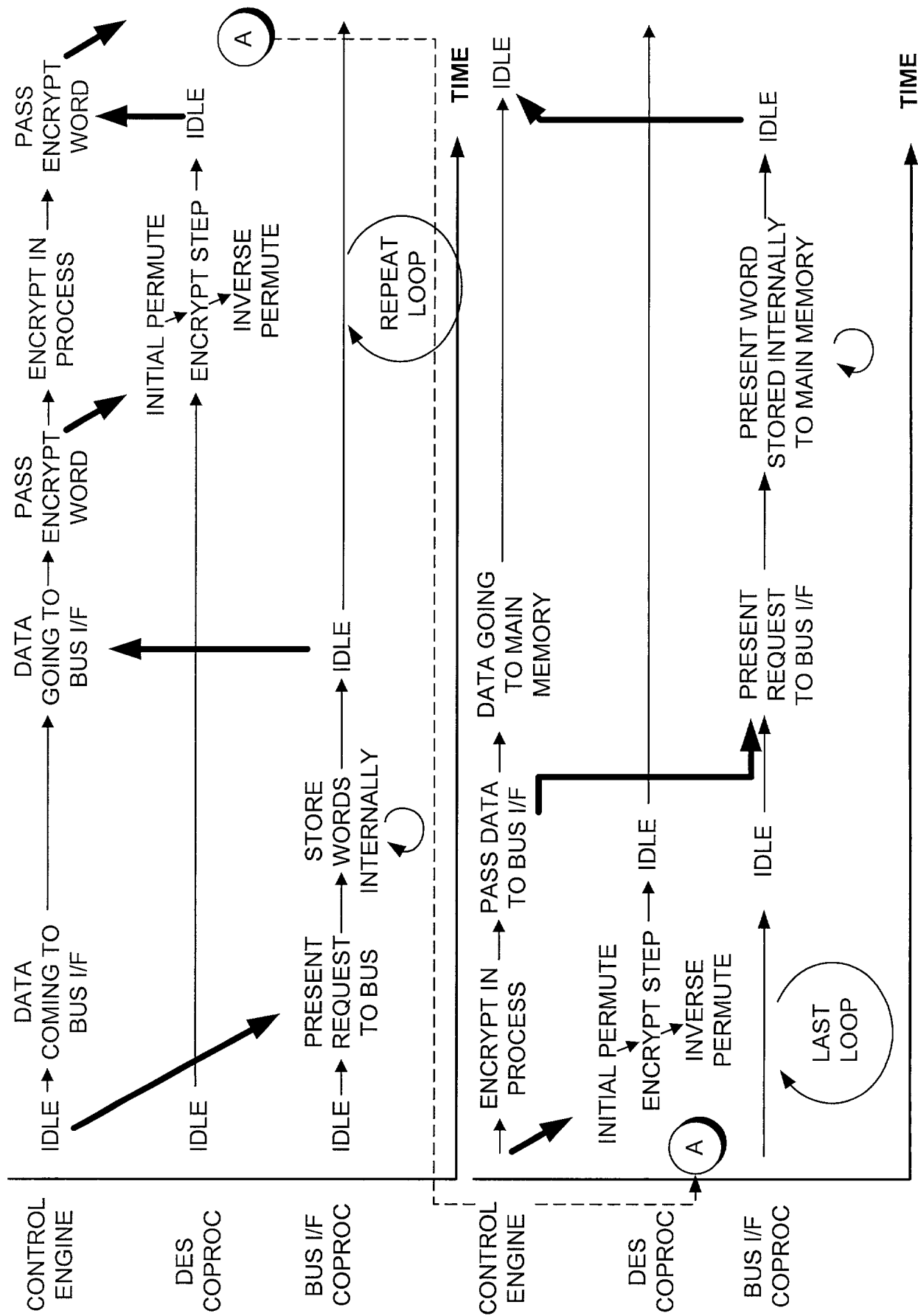
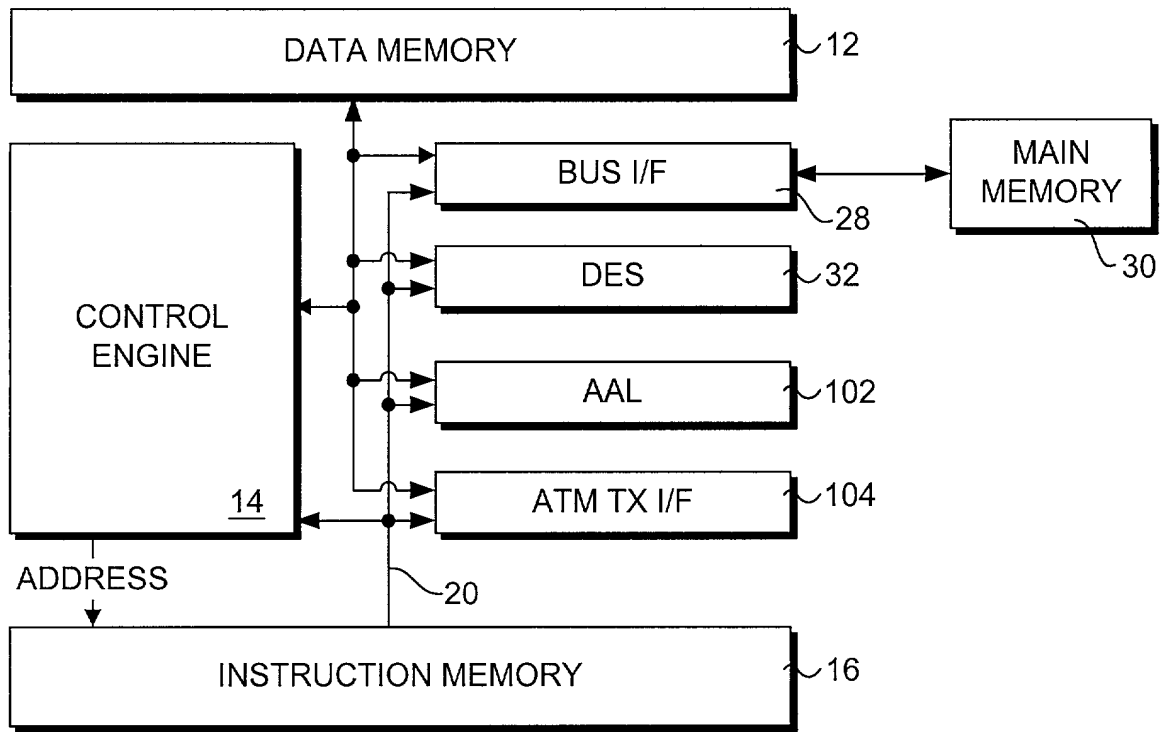
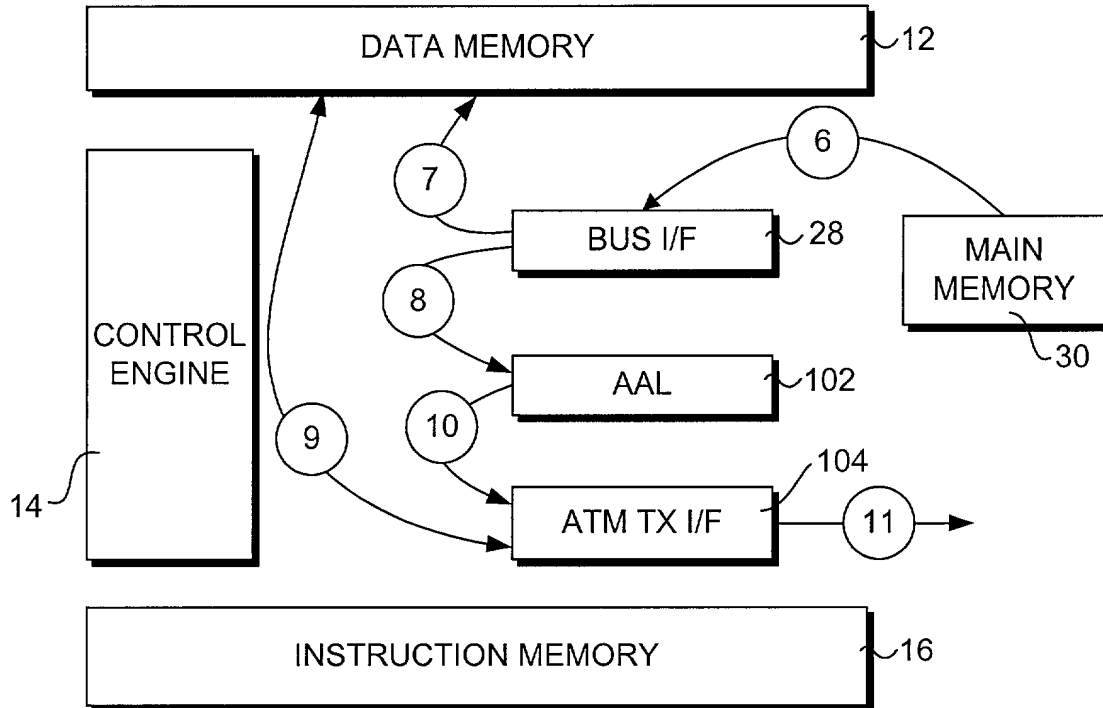


FIG. 9

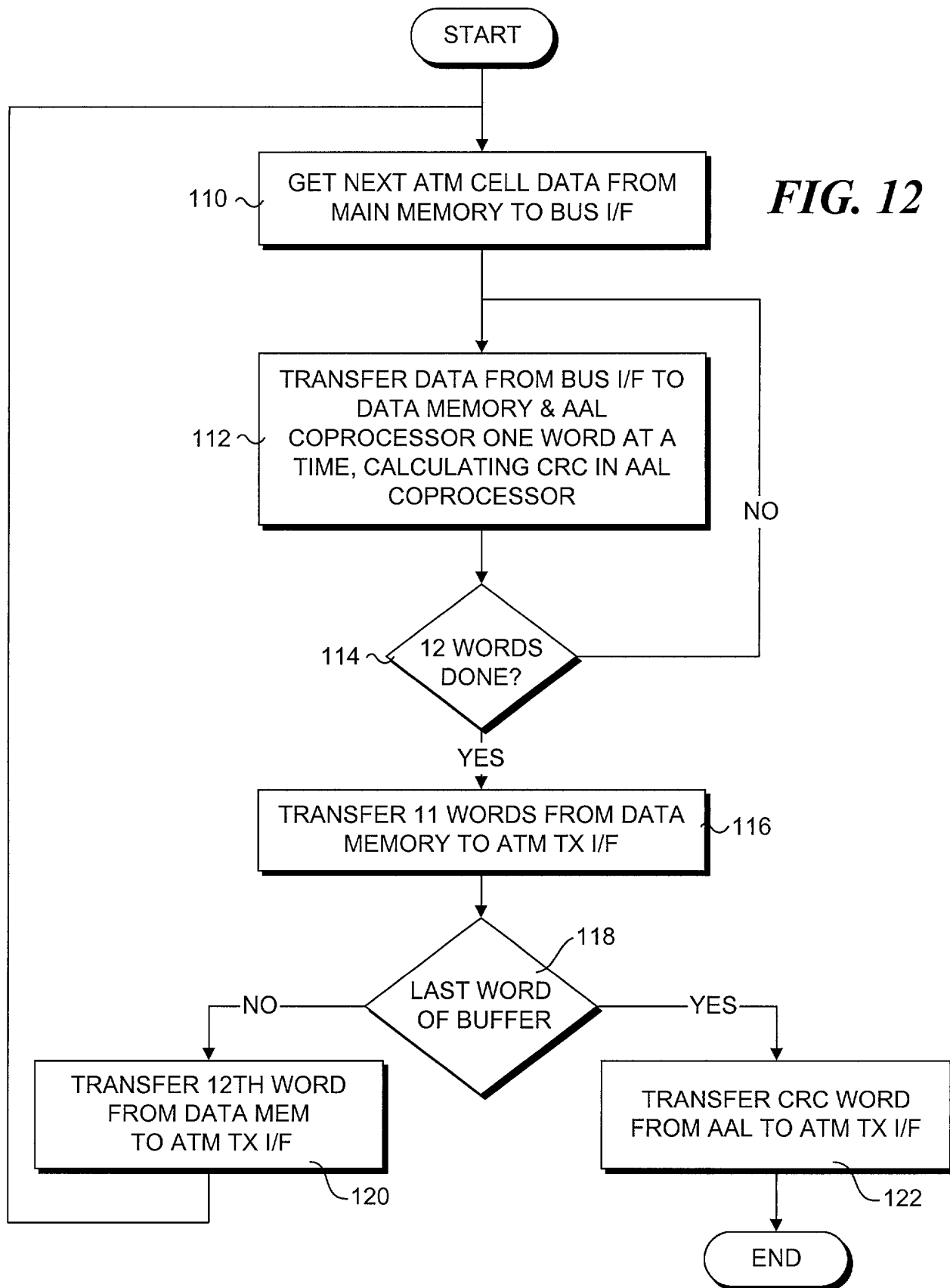


**FIG. 10**



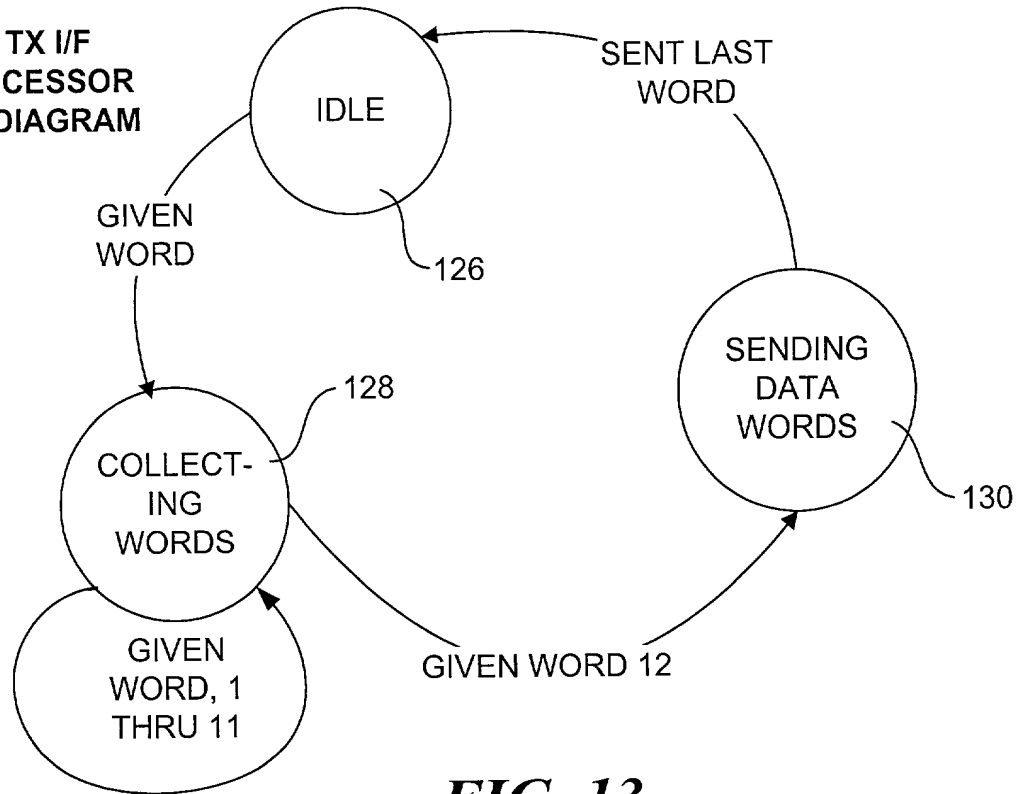
**FIG. 11**



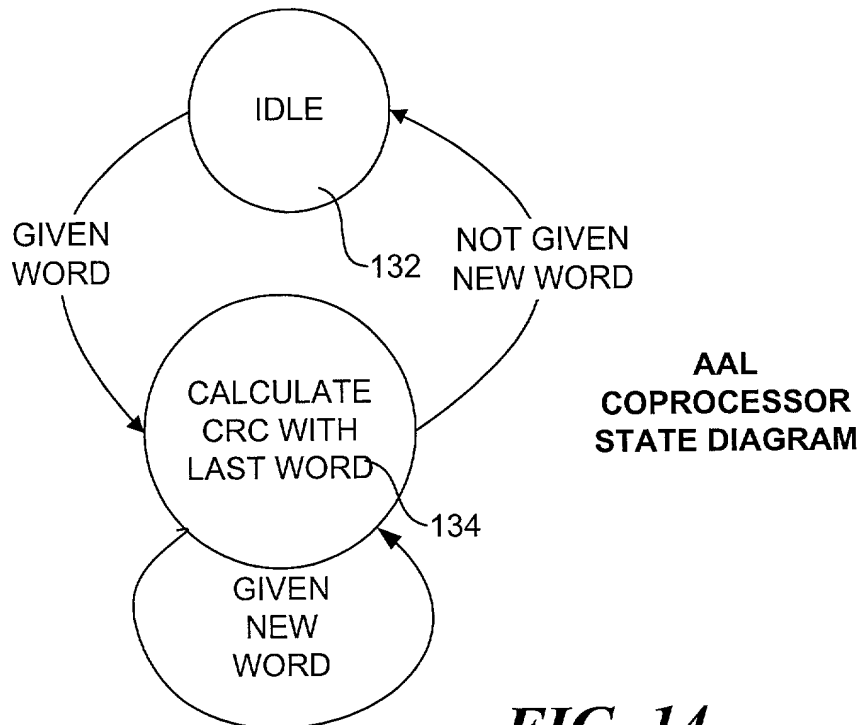


**FIG. 12**

**ATM TX I/F  
COPROCESSOR  
STATE DIAGRAM**

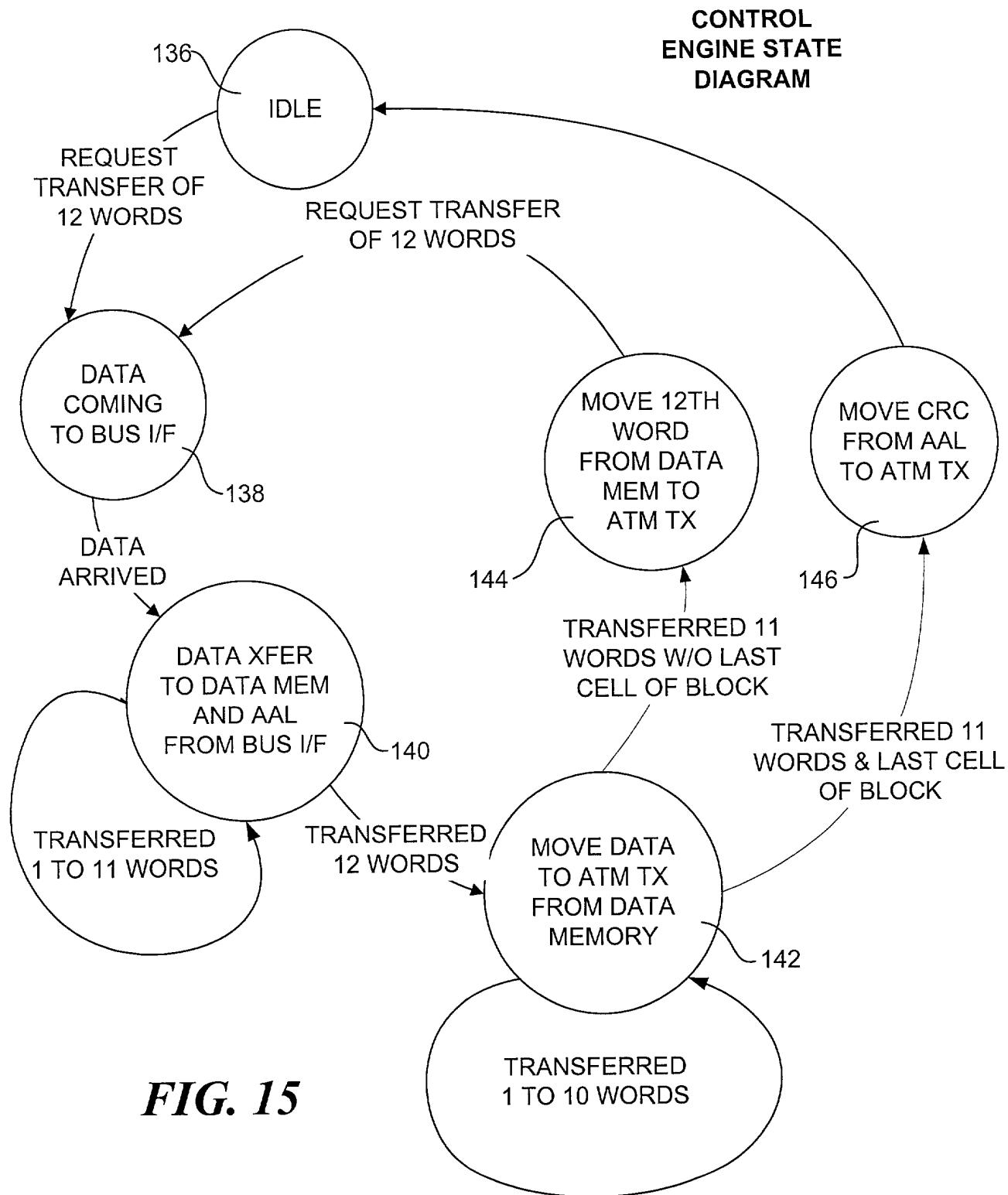


**FIG. 13**



**AAL  
COPROCESSOR  
STATE DIAGRAM**

**FIG. 14**



**FIG. 15**